

ABSTRACT OF THE DISCLOSURE

A semiconductor integrated circuit includes a variable region to be subjected to a layout modification in conjunction with a change of a circuit component within the variable region; and a fixed region that is free from the layout modification, and includes a circuit such as a CPU core and peripheral functional section whose signal transfer characteristics are known when the circuit is considered as a closed circuit. It can reduce the manpower required for the layout modification and characteristic verification and evaluation of the circuit involved in the layout modification.